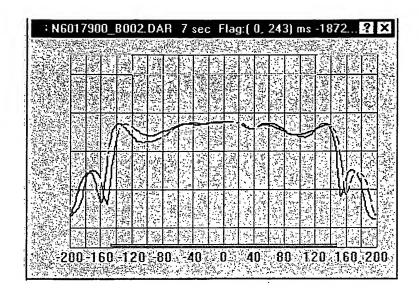
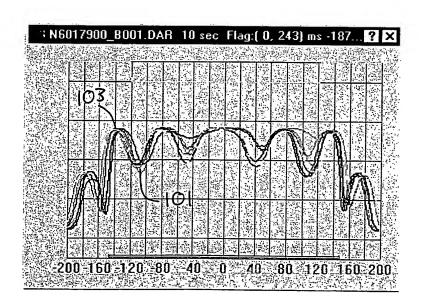
Docket No. N1085-00261 Inventors: Wan-Cheng Yang "In-Line Wafer Surface Mapping" Sheet 1 of 8

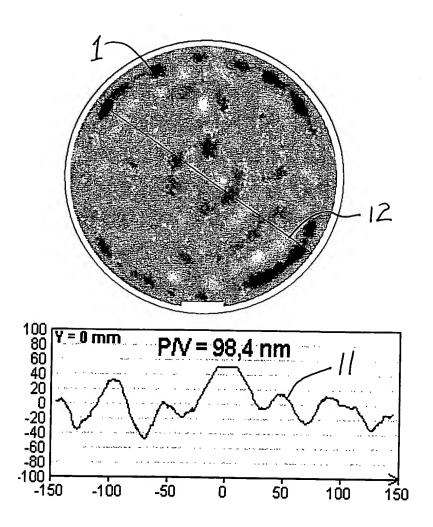
FIGURE 1 A





Docket No. N1085-00261 Inventors: Wan-Cheng Yang "In-Line Wafer Surface Mapping" Sheet 2 of 8

FIGURE 2



Docket No. N1085-00261 Inventors: Wan-Cheng Yang "In-Line Wafer Surface Mapping" Sheet 3 of 8

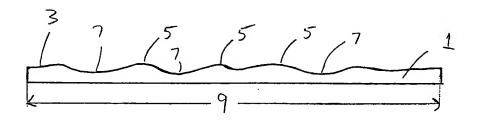


FIGURE 3.

Docket No. N1085-00261 Inventors: Wan-Cheng Yang "In-Line Wafer Surface Mapping" Sheet 4 of 8

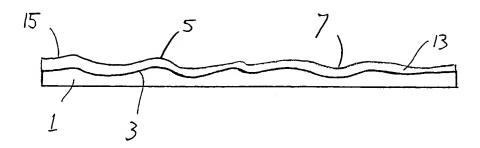


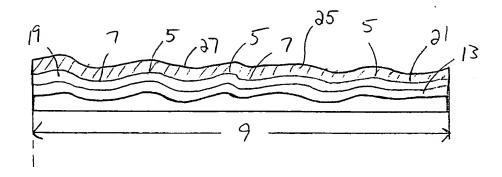
FIGURE 4

Docket No. N1085-00261 Inventors: Wan-Cheng Yang "In-Line Wafer Surface Mapping" Sheet 5 of 8



F16 5

Docket No. N1085-00261 Inventors: Wan-Cheng Yang "In-Line Wafer Surface Mapping" Sheet 6 of 8



F16 6

Docket No. N1085-00261 Inventors: Wan-Cheng Yang "In-Line Wafer Surface Mapping" Sheet 7 of 8

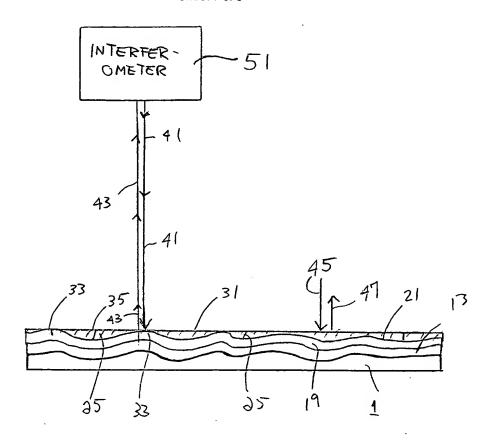


FIGURE 7

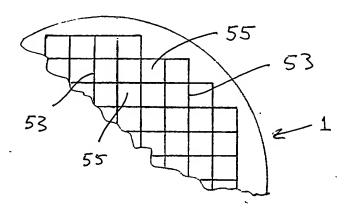


FIGURE 8

Docket No. N1085-00261 Inventors: Wan-Cheng Yang "In-Line Wafer Surface Mapping" Sheet 8 of 8

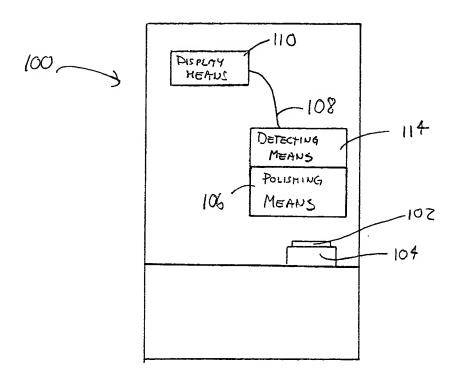


FIGURE 9